

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

(Use several sheets if necessary)

SERIAL NO.
10/670,062

APPLICANT
Buchert et al.

**GROUP
2193**

[illegible]

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

DATE CONSIDERED
11/26/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				ATTY. DOCKET NO. I-2-0344.1US		SERIAL NO. 10/670,062	
				APPLICANT Buchert et al.			
				FILING DATE September 24, 2003		GROUP 2193	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TM		3,535,694	10/1970	Anacker et al.			
↓		4,811,210	03/1989	Maculay et al.			
		5,974,435	10/1999	Abbott et al.			
		6,141,673	10/2000	Thayer et al.			
		6,298,366	10/2001	Gatherer et al.			
		6,317,770	11/2001	Lim et al.			
		6,334,176	12/2001	Scales III et al.			
		6,366,937	04/2002	Shridhar et al.			
FOREIGN PATENT DOCUMENTS /Tan Mai/							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
TM		Cathoor et al., "Efficient Microcoded Processor Design for Fixed Rate DFT and FFT1", Journal of VLSI Signal Processing Systems For Signal, Image, and Video Technology, Vol. 1, No. 4, April 1, 1990, pp. 287-306.					

EXAMINER /Tan Mai/	DATE CONSIDERED 11/26/2006
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